What is claimed:

1. A plasma display panel, comprising:

a first substrate;

a plurality of first substrate electrode pairs formed on the first substrate in a first direction;

a second substrate;

a plurality of second substrate electrodes formed on the second substrate in a second direction crossing the first substrate electrode pairs in the first direction;

a dielectric layer formed on the second substrate and the second substrate electrodes;

barriers formed on the dielectric layer aligned between the second substrate electrodes;

a phosphor layer formed on the dielectric layer including the barriers; and

a plurality of projections formed on the phosphor layer between the barriers.

2. The plasma display panel of claim 1, wherein the plurality of projections are separated from each of the plurality of barriers at the lower portion, the upper portion, or the lower and upper portions of each barrier.

3. The plasma display panel of claim 1, wherein at least one of the plurality of projections is tapered.

4. The plasma display panel of claim 1, wherein at least one of the plurality of projections has a conical shape.

5. The plasma display panel of claim 1, wherein at least one of the plurality of projections has a polygonal shape.

6. The plasma display panel of claim 1, wherein at least one of the plurality of projections has a height equivalent to the height of at least one of the barriers.

7. A plasma display panel, comprising:

a first substrate;

a plurality of first substrate electrode pairs formed on the first substrate in a first direction;

a first dielectric layer formed on the first substrate and the first substrate electrode pairs;

a second substrate;

a plurality of second substrate electrodes formed on the second substrate in a second direction to cross the first substrate electrode pairs in the first direction;

a second dielectric layer formed on the second substrate and the second substrate electrodes; and

barriers formed on the second dielectric layer, wherein an exhaust path is formed between the first and second dielectric layers.

8. The plasma display panel of claim 7, further comprising:

a phosphor layer formed on the second dielectric layer and the barriers; and

a plurality of projections formed on the phosphor layer between the barriers, wherein the exhaust path is between the barriers, the plurality of projections and the first and second dielectric layers.

9. The plasma display panel of claim 8, wherein the plurality of projections are separated from the barriers by the exhaust path.

10. The plasma display panel of claim 7, wherein the exhaust path comprises a groove formed in the second dielectric layer.

11. The plasma display panel of claim 10, wherein the groove is formed in a discharge space between the barriers forming the exhaust path between the barriers.

12. The plasma display panel of claim 10, wherein the groove is formed above the barriers forming the exhaust path above the barriers.